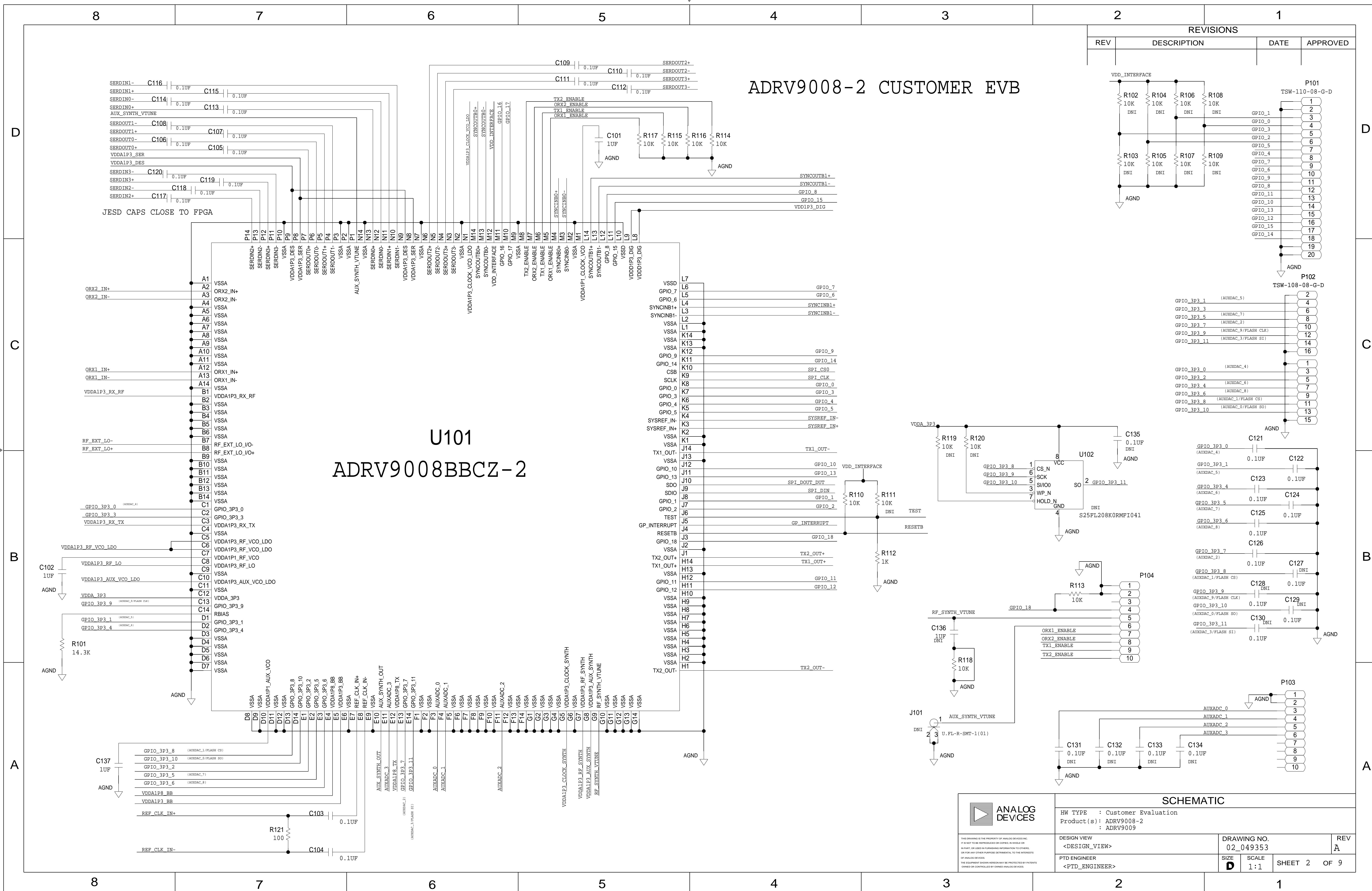
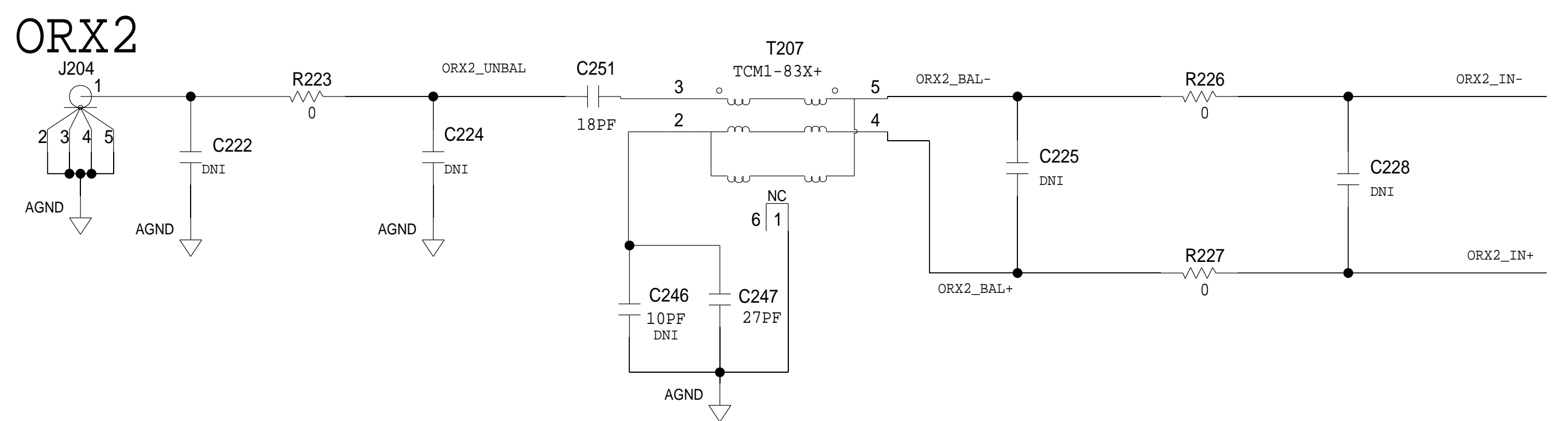
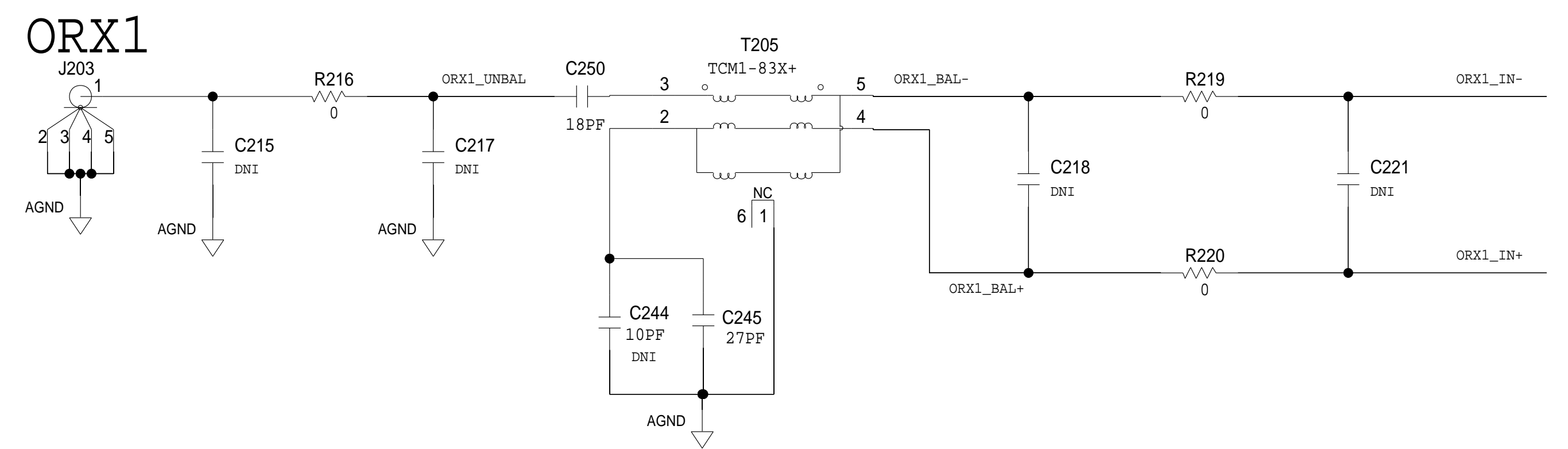
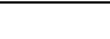
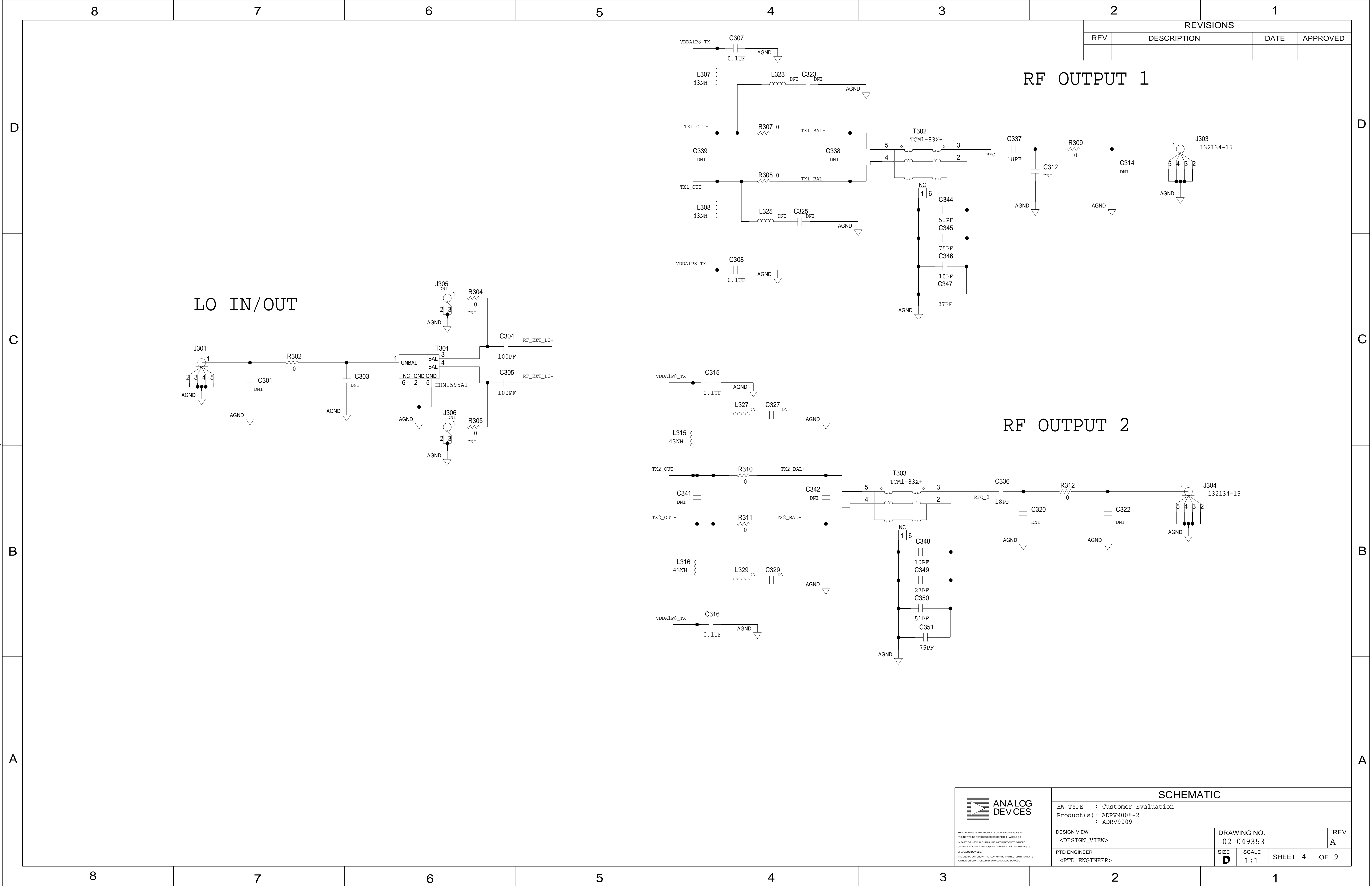


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D	<div>THIS DRAWING IS THE PROPERTY OF ANALOG DEVICES INC. IT IS NOT TO BE REPRODUCED OR COPIED, IN WHOLE OR IN PART, OR USED IN FURNISHING INFORMATION TO OTHERS, OR FOR ANY OTHER PURPOSE DETRIMENTAL TO THE INTERESTS OF ANALOG DEVICES. THE EQUIPMENT SHOWN HEREON MAY BE PROTECTED BY PATENTS OWNED OR CONTROLLED BY ANALOG DEVICES.</div>					<div>JUMPER TABLE</div> <table><tr><td>JP#</td><td>ON</td><td>OFF</td></tr><tr><td>1</td><td></td><td></td></tr><tr><td>2</td><td></td><td></td></tr><tr><td>3</td><td></td><td></td></tr><tr><td>4</td><td></td><td></td></tr><tr><td>5</td><td></td><td></td></tr></table> <div>* SEE ASSEMBLY INSTRUCTIONS</div>			JP#	ON	OFF	1			2			3			4			5			<div>REVISIONS</div> <table><tr><td>REV</td><td>DESCRIPTION</td><td>DATE</td><td>APPROVED</td></tr><tr><td>A</td><td>INITIAL RELEASE</td><td>04AUG17</td><td>T. KARPUS</td></tr><tr><td>B</td><td>AS PER ECR-074509</td><td>19JAN18</td><td>N. PARIKH</td></tr></table>									REV	DESCRIPTION	DATE	APPROVED	A	INITIAL RELEASE	04AUG17	T. KARPUS	B	AS PER ECR-074509	19JAN18	N. PARIKH																																	
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					<div>SCHEMATIC</div> <div>UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES</div> <table><tr><td>TEMPLATE ENGINEER -</td><td>DATE</td><td colspan="3">DRAWING NO. 02_049353</td><td>REV. A</td></tr><tr><td>HARDWARE SERVICES -</td><td></td><td colspan="4"></td></tr><tr><td>HARDWARE SYSTEMS -</td><td></td><td colspan="4"></td></tr><tr><td>TEST ENGINEER -</td><td></td><td colspan="4"></td></tr><tr><td>COMPONENT ENGINEER -</td><td></td><td colspan="4"></td></tr><tr><td>TEST PROCESS -</td><td></td><td colspan="4"></td></tr><tr><td>HARDWARE RELEASE -</td><td></td><td colspan="4"></td></tr><tr><td>DESIGNER -</td><td>04AUG17</td><td>MASTER PROJECT TEMPLATE TBD</td><td>TESTER TEMPLATE no_template</td><td colspan="2"></td></tr><tr><td>PTD ENGINEER &lt;PTD_ENGINEER&gt;</td><td>04AUG17</td><td colspan="2">TOLERANCES</td><td colspan="2"></td></tr><tr><td>CHECKER -</td><td>04AUG17</td><td>DECIMALS X.XX +0.010 X.XXX +-0.005</td><td>FRACTIONS +/-1/32</td><td>ANGLES +2</td><td></td></tr><tr><td colspan="2"></td><td>SIZE D</td><td>SCALE 1:1</td><td>CODE ID NO. CodeID</td><td>SHEET 1 OF 9</td></tr></table>										TEMPLATE ENGINEER -	DATE	DRAWING NO. 02_049353			REV. A	HARDWARE SERVICES -						HARDWARE SYSTEMS -						TEST ENGINEER -						COMPONENT ENGINEER -						TEST PROCESS -						HARDWARE RELEASE -						DESIGNER -	04AUG17	MASTER PROJECT TEMPLATE TBD	TESTER TEMPLATE no_template			PTD ENGINEER <PTD_ENGINEER>	04AUG17	TOLERANCES				CHECKER -	04AUG17	DECIMALS X.XX +0.010 X.XXX +-0.005	FRACTIONS +/-1/32	ANGLES +2				SIZE D	SCALE 1:1	CODE ID NO. CodeID	SHEET 1 OF 9
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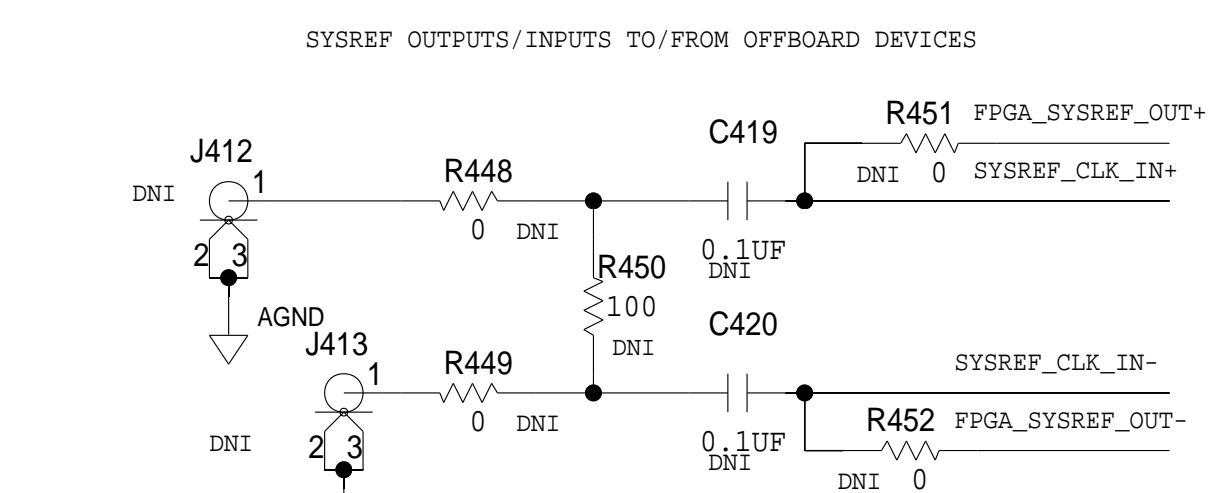




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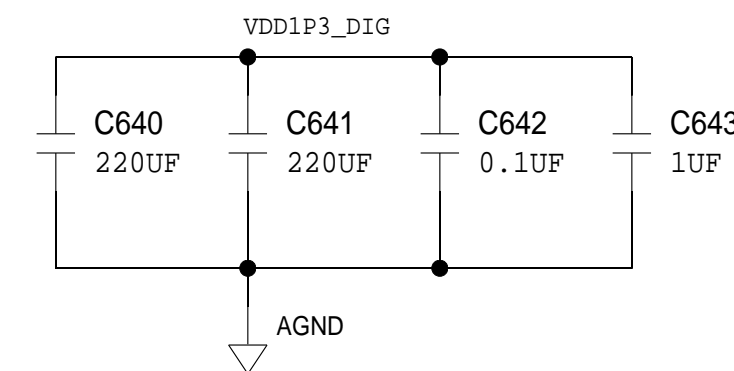
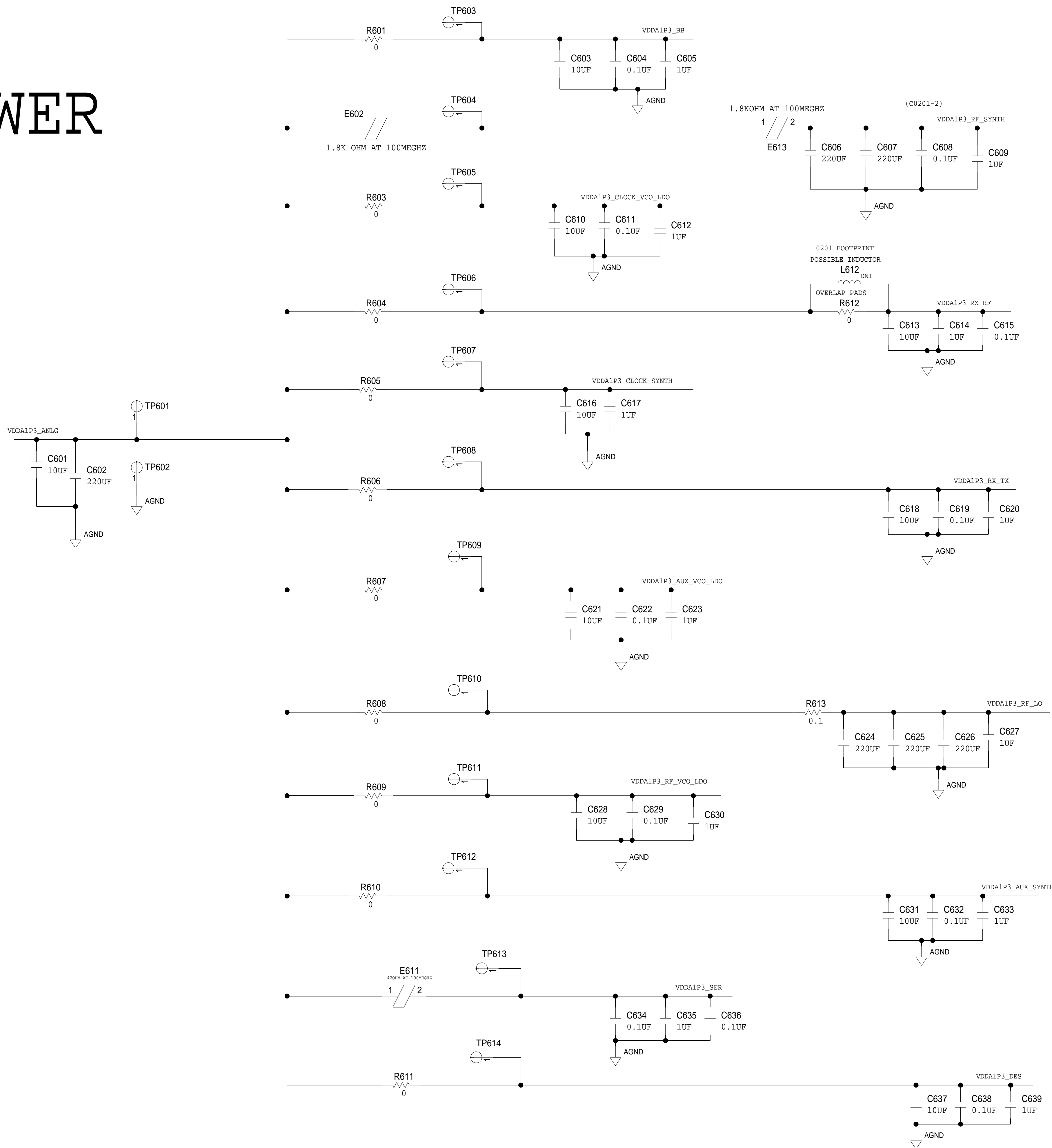
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# POWER

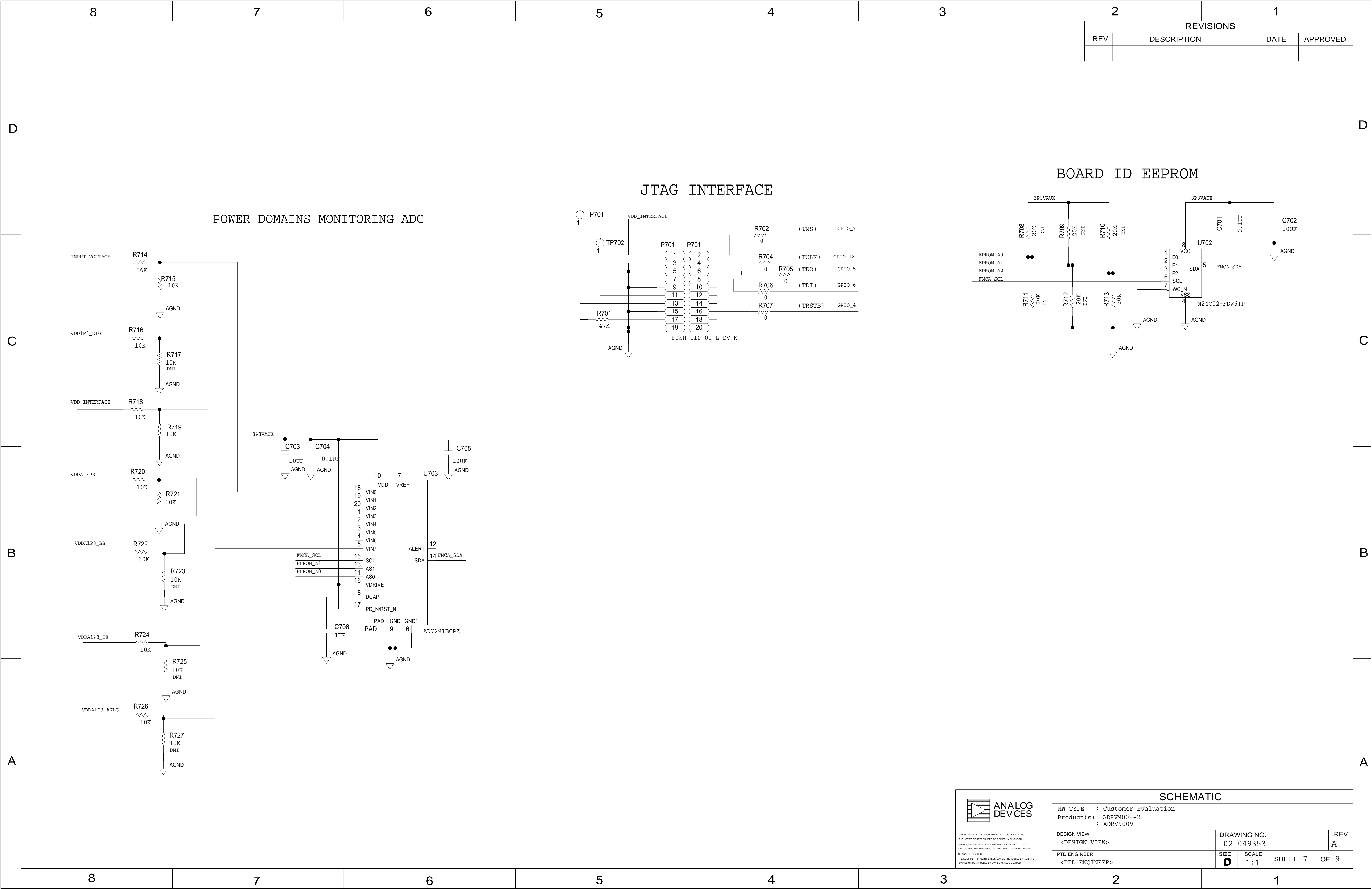
1.3VDC



## REVISIONS

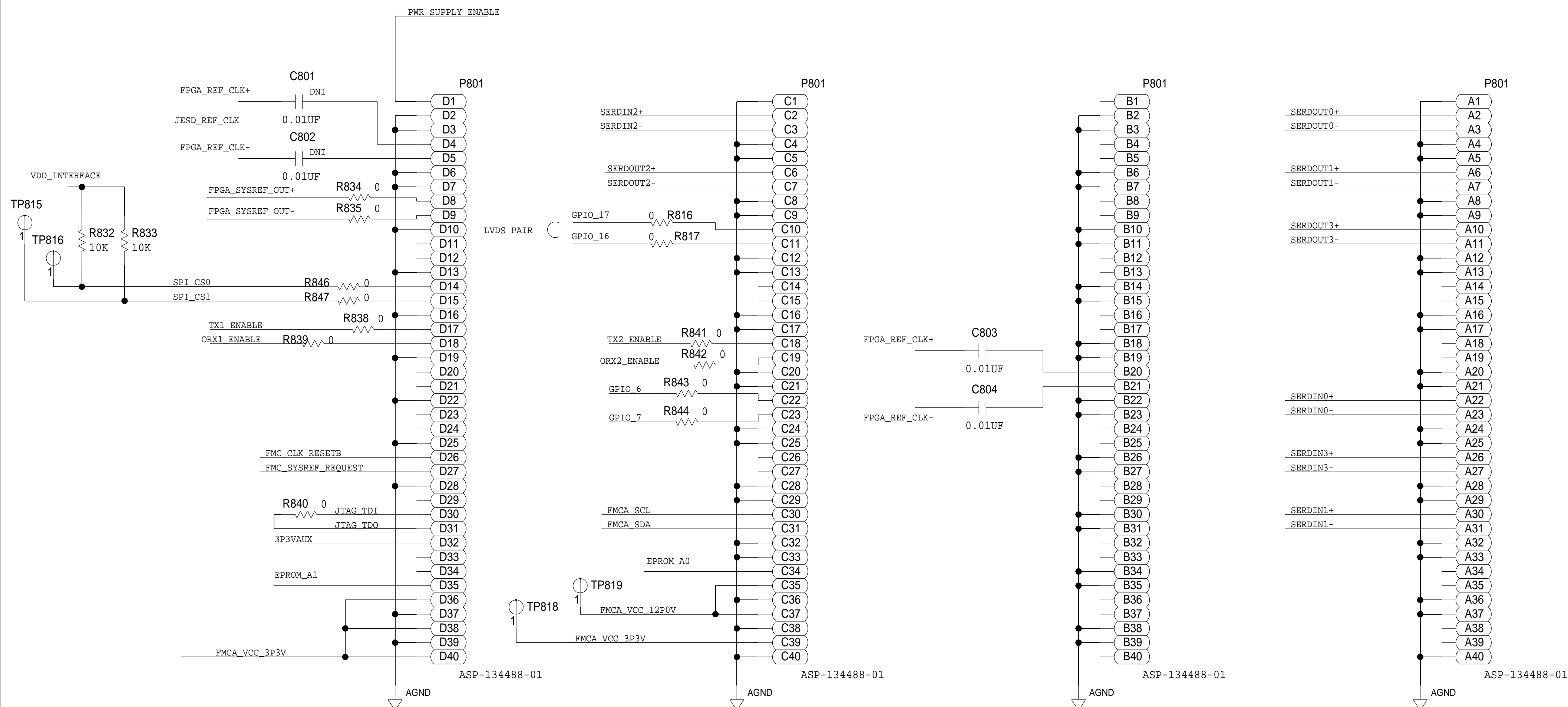
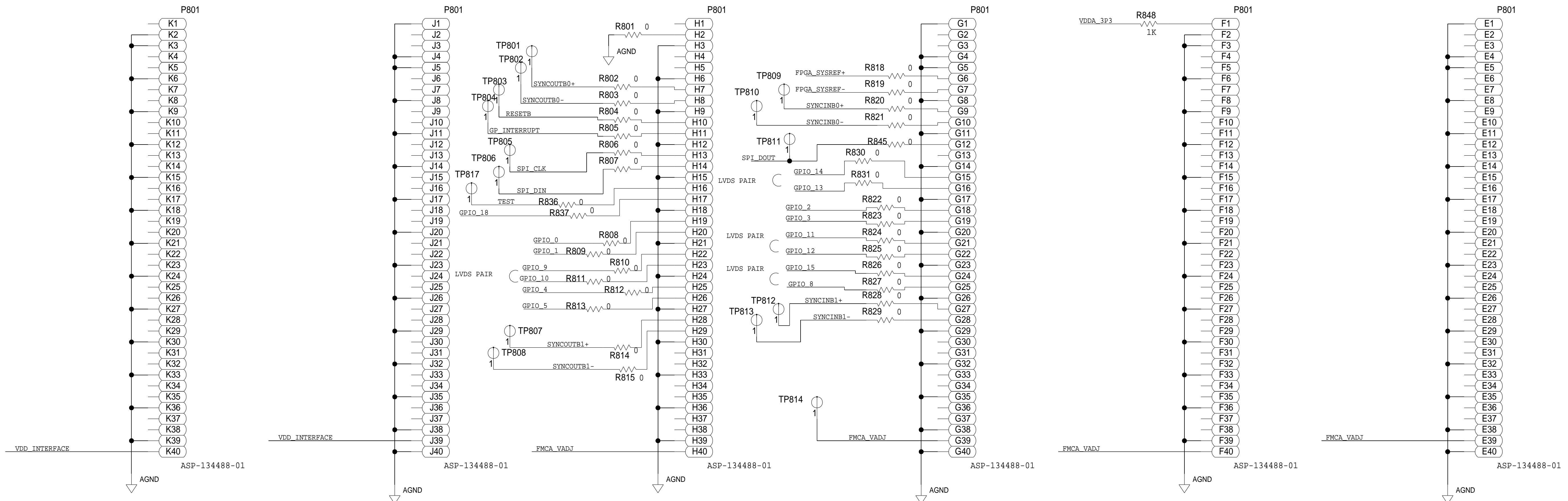
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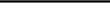
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# FPGA CONNECTIONS

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

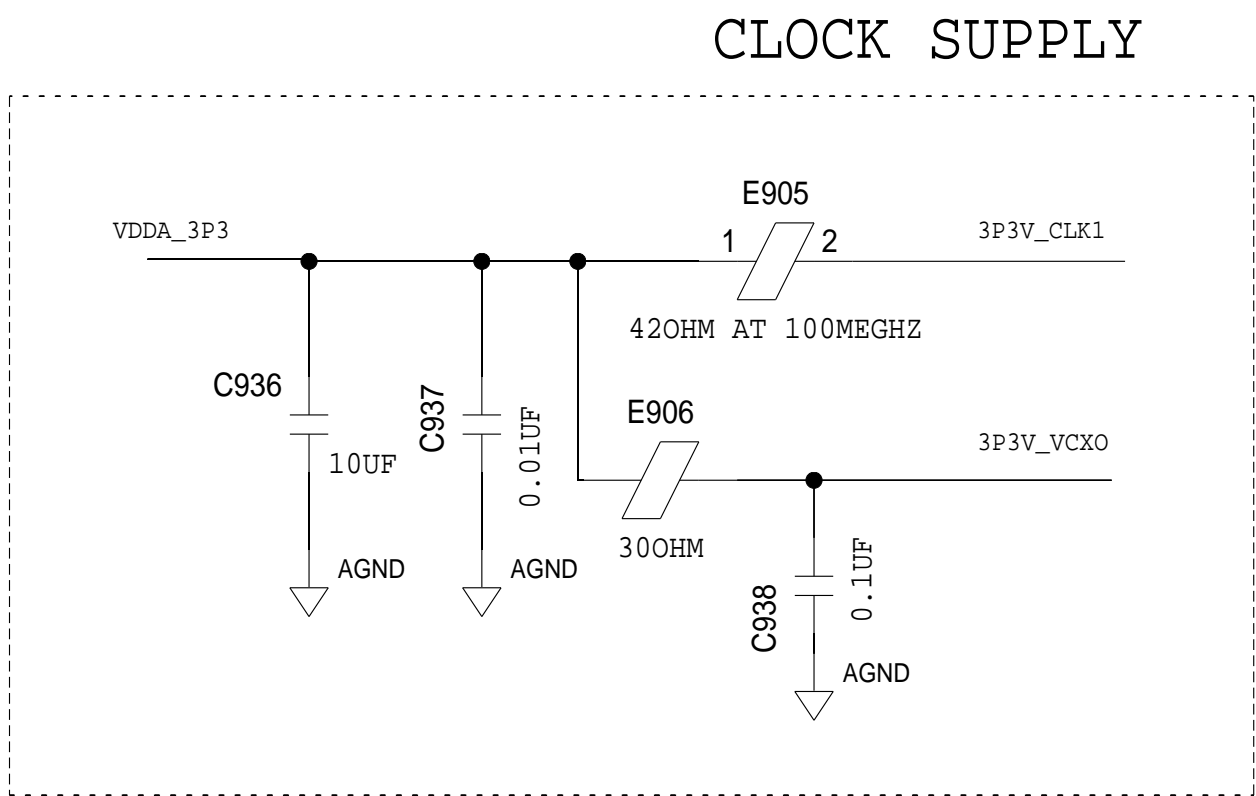
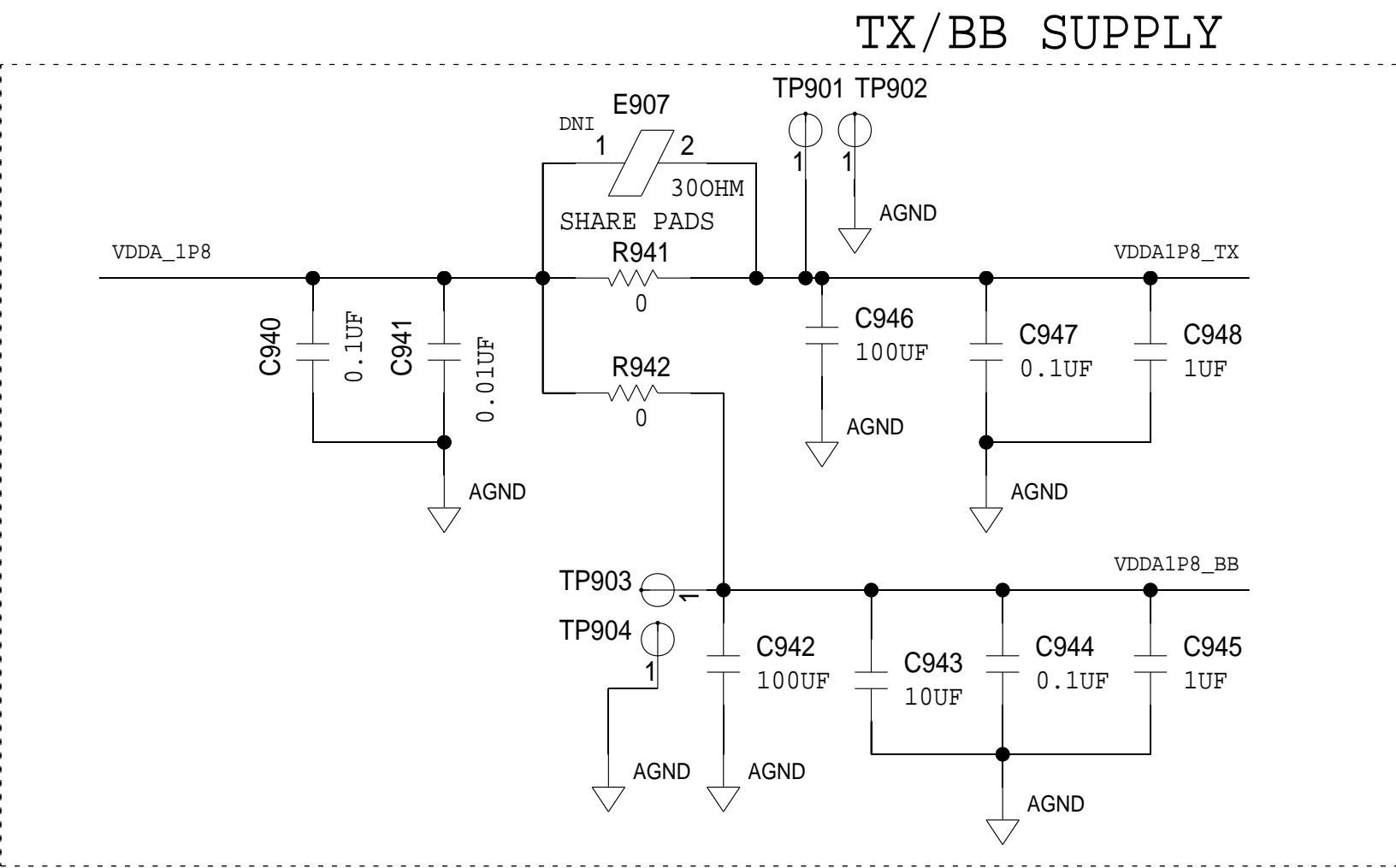
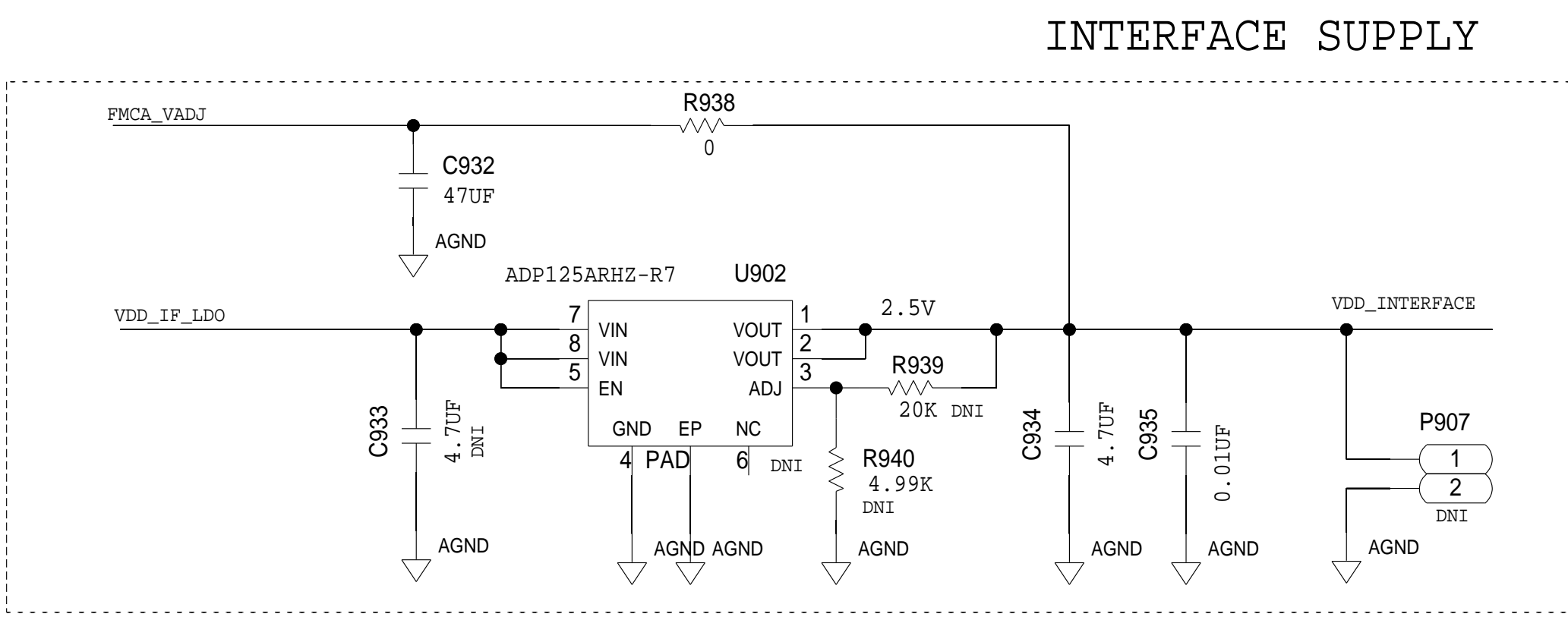
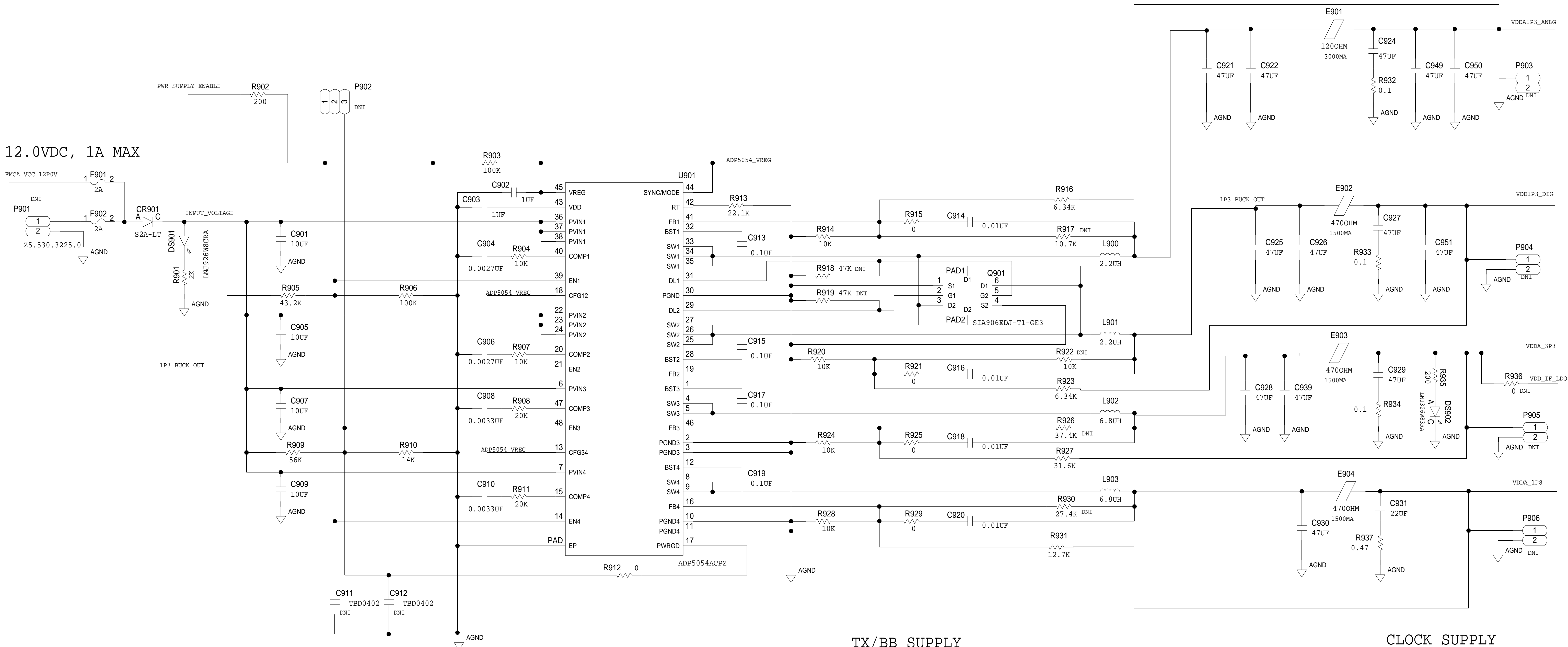


 <b>ANALOG DEVICES</b>	<h1 style="text-align: center;">SCHEMATIC</h1>			
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	PTD ENGINEER <b>&lt;PTD_ENGINEER&gt;</b>	SIZE <b>D</b>	SCALE 1:1	SHEET 8 OF 9



POWER SUPPLY

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED



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DESIGN VIEW <DESIGN_VIEW>	DRAWING NO. 02_049353	REV A	
PTD ENGINEER <PTD_ENGINEER>	SIZE D	SCALE 1:1	SHEET 9 OF 9